

WEST Search History

DATE: Friday, September 28, 2007

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L13	l11 and L12	4
<input type="checkbox"/>	L12	((output near2 buffer) with (two near5 (multiplexer or (multi adj plexer) or mux)))[clm]	7
<input type="checkbox"/>	L11	((input near2 buffer) with (multiplexer or (multi adj plexer) or mux))[clm]	124
<input type="checkbox"/>	L10	((input near2 buffer) and (output near2 buffer) and (three near5 (multiplexer or (multi adj plexer) or mux)))[clm]	0
<input type="checkbox"/>	L9	l2 and l3 and l4 and l8	0
<input type="checkbox"/>	L8	(interface near3 buffer)[clm]	947
<input type="checkbox"/>	L7	(dedicated with interface with track)[clm]	1
<input type="checkbox"/>	L6	(dedicated with (interface near3 track))[clm]	0
<input type="checkbox"/>	L5	((mask adj programm\$) with (interface near3 track))[clm]	0
<input type="checkbox"/>	L4	(asic or (application adj specific adj integrated adj circuit))[clm]	2702
<input type="checkbox"/>	L3	(fpga or (field adj programmable adj gate adj array))[clm]	1849
<input type="checkbox"/>	L2	(interface near3 architecture)[clm]	292
		<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>	
<input checked="" type="checkbox"/>	L1	(interface near3 architecture)[clm]	501

END OF SEARCH HISTORY